



Form PTO-1449 (Rev. 08-32)						U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. ACT-299COA		Serial No. 10/821,533	
Information Disclosure Statement by Applicant							Applicant: Kundu et al.				
(Use several sheets if necessary)							Filed: April 8, 2004		Group: 2183		
U.S. Patent Documents											
Init.		Document No.	Date	Name	Class	Subclass	Filing Date				
n	1	4,758,745	07/19/1988	El Gamal (Elgamal) et al.	307	465	09/19/1986				
w	2	B1 4,758,745	11/15/1994	El Gamal (Elgamal) et al.	307	465	09/19/1986				
v	3	4,870,302	09/26/1989	Freeman	307	465	02/19/1988				
w	4	5,970,254	10/19/1999	Cooke et al.	395	800.37	06/27/1997				
Foreign Documents											
Init.		Document No.	Date	Country	Class	Subclass	Translation				
n	5	EP 0 840 455 A2	05/06/1998	Europe	H03K	19/177		X			
n	6	EP 0 840 455 A3	11/15/2000	Europe	H03K	19/177		X			
wk	7	DE 198 19 505 A1	11/04/1999	German	G06F	19/00		X			
n	8	WO 00/22546 A2	04/20/2000	PCT (United States)	G06F	15/78		X			
n	9	WO 00/22546 A3	07/20/2000	PCT (United States)	G06F	15/78		X			
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)											
w	10	CY3650, CY3651 USB Developer's Kit Preliminary, Cypress Semiconductor Corporation, pp. 1 & 2, October 1996 – Revised July 1997.									
n	11	“Mit programmierbarer Logik verheiratet”, Elektronik, Francis Verlag GMBH, Munchen, DE, Vol. 47, No. 7, P. 38, March 31, 1998.									
w	12	Integrated Quad UART and PCI interface, OX16PC1954 Data Sheet Revision 1.3, Oxford Semiconductor, pp. 1-72, February 1999.									
v	13	OX16PC1954 Intelligent QUAD Channel UART with PCI, Oxford Semiconductor Product Catalog, pp. 12–13, 1999 (no month).									
w	14	“Inventra M8051 8-BIT Microcontroller”, [Internet] www .mentor.com/inventra, Mentor Graphics, pp. 1 & 2, October 1999.									
v	15	“Inventra M8051 Warp 8-BIT Microcontroller”, [Internet] www .mentor.com/inventra, Mentor Graphics, pp. 1 & 2, January 2000..									
w	16	AMBA™ Specification, (Rev 2.0), ARM Limited, pp. 1 – Index iv, 1999 (no month).									
n	17	AMBA™ Overview, ARM Processors and Peripherals, ARM Limited, pp. 1 & 2, 2000 (no month).									
Examiner						Date Considered					
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.											

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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)				
✓	18	Triscend E5 Configurable System-on-Chip Family, Triscend Corporation, 1-90, i-ii; 2000 (no month).		
✓	19	AN07 Using Keil Development tools with Triscend FastChip and the E5 CsoC Family, Triscend Corporation, pp. 1-20, 2000 (no month).		
✓	20	CY7C64013, CY7C64113 Full-speed USB (12 Mbps) Function Preliminary, Cypress Semiconductor Corporation, pp. 1-2, February 4, 2000.		
✓	21	Phil Harvey, "Triscend leads a hybrid chip charge", [Internet] Inside Tech column, <u>Red Herring</u> , pp. 1-3, February 4, 2000.		
✓	22	Inventra™ M1284H-A1 Host Parallel Port, Datasheet, [Internet] www mentor.com/inventra, Mentor Graphics, pp. 1 & 2, June 2000.		
✓	23	Inventra™ M8051 E-Warp 8-Bit Microcontroller with On-Chip Debug, [Internet], www mentor.com/inventra, Mentor Graphics, pp. 1 & 2, June 2000.		
✓	24	Markus Levy, "Processors drive (or dive) into programmable-logic devices", <u>EDN</u> , pp. 107-108, 110, 112, 114, July 20, 2000.		
✓	25	Field Marshal P200 Technology Overview, [Internet] http://www stellima.co.uk/proj200view.htm, pp. 1-2, July 26, 2000.		
✓	26	All about UARTs, [Internet] http:// consumer.3com.com/modem/documents/10589.htm, 3Com Corp., pp. 1-2, July 26, 2000.		
✓	27	PCT/US 01/27130, ACT-299PCT, Actel Corporation, copy of International Search Report, 5 pages, mailed 8/25/2003.		
Examiner			Date Considered	
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.				